



Sheet 1 of 1

Form PTO-1449		Docket No. P01,0365		Serial No. 10/040,395	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION (use several sheets if necessary)				Cheisan J. Yue et al.	
				Filing Date January 7, 2002	
Group Art Unit unknown					
U.S. PATENT DOCUMENTS					
Examiner's Initials		Document Number	Date	Name	Class
<i>SP</i>	AA	5,965,912	10/12/99	Stolfa et al.	
<i>SP</i>	AB	5,045,966	09/03/91	Alter	
<i>SP</i>	AC	6,172,378 B1	01/09/01	Hull et al.	
	AD				
	AE				
	AF				
FOREIGN PATENT DOCUMENTS					
		Document Number	Date	Country	Class
					Subclass
					Translation
					Yes
					No
<i>SP</i>	AG	EP 1 024 538 A1	08/02/2000	Europe	
	AH				
	AI				
	AJ				
	AK				
	AL				
	AM				
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	AP				
	AQ				
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)					
<i>SP</i>	AR	Mathi, "A SOI-CMOS Process for VLSI Technology", Navy Technical Disclosure Bulletin 10 (1984) Sept., No. 1, pgs. 117-121.			
<i>SP</i>	AS	Svelto et al., "A Three Terminal Varactor for RFIC's in Standard CMOS Technology", IEEE Transactions on Electron Devices, Vol. 47, No. 4, April 2000, pgs. 893-895.			
	AT				
	AU				
	AV				
	AW				
	AX				
Examiner <i>Shoung-Her</i>		Date Considered <i>12/22/05</i>			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					

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